

L Number	Hits	Search Text	DB	Time stamp
-	0	ISDN and (((receiv\$3 same transmit\$4) same buffer same interrupt same microporcessor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 06:55
-	30610	ISDN	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 06:55
-	385	ISDN and (buffer same controller same (upload\$3 receiv\$3) same (transmit\$4 download\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 06:56
-	309	(ISDN and (buffer same controller same (upload\$3 receiv\$3) same (transmit\$4 download\$3))) and @ad < 19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 06:58
-	4	memory same (settable adj size\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 08:00
-	3884	(buffer memory fifo) same (mcu (memory adj ((control adj unit) controller))) same register\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 12:07
-	79	((buffer memory fifo) same (mcu (memory adj ((control adj unit) controller))) same register\$1) and isdn	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 08:04
-	1151	((((buffer memory fifo) same (mcu (memory adj ((control adj unit) controller))) same register\$1) and isdn) and @ad < 1998 "0918"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 08:04
-	65	((((buffer memory fifo) same (mcu (memory adj ((control adj unit) controller))) same register\$1) and isdn) and @ad < 19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 08:04
-	79	((buffer memory fifo) same (mcu (memory adj ((control adj unit) controller))) same register\$1) and isdn	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 08:04
-	65	((((buffer memory fifo) same (mcu (memory adj ((control adj unit) controller))) same register\$1) and isdn) and @ad < 19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 08:29
-	504	lapd and isdn	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 08:15

-	3	(lapd and lsdn) and (buffer adj supervi\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 08:29
-	230	(lapd and lsdn) and (control same (buffer memory))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 08:29
-	58	(lapd and lsdn) and (control same (buffer memory) same register)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 08:29
-	43	((lapd and lsdn) and (control same (buffer memory) same register)) and @ad < 19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 08:43
-	374	(memory fifo buffer) same register\$1 same ((variable settable) with size)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 08:46
-	2	((memory fifo buffer) same register\$1 same ((variable settable) with size)) same (communication ISDN network) same control\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 08:45
-	202	register\$1 same(((variable settable) with size) with (memory fifo buffer))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 08:50
-	62	register\$1 same(((variable settable) adj size) with (memory fifo buffer))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 08:50
-	5	register\$1 same(((variable settable) adj size) adj (memory fifo buffer))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 08:50
-	0	register\$1 same(((settable) adj size) adj (memory fifo buffer))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 08:50
-	0	register\$1 same(((settable) adj size) with (memory fifo buffer))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 08:50
-	0	register\$1 same(((settable) adj size) with (memory fifo buffer))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 08:50

-	2	register\$1 same(((settable) with size) with (memory fifo buffer))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 08:51
-	3067	control\$4 same Interface same (data adj link) same Interface	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 09:41
-	375	(control\$4 same Interface same (data adj link) same interface) same (memory buffer fifo) same (cpu microprocessor processor) same receiv\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 09:42
-	91	((control\$4 same interface same (data adj link) same interface) same (memory buffer fifo) same (cpu microprocessor processor) same receiv\$3) same register\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 09:43
-	76	((control\$4 same interface same (data adj link) same interface) same (memory buffer fifo) same (cpu microprocessor processor) same receiv\$3) same register\$1) and @ad < 19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 09:43
-	17677	memory same control\$4 same register same (number quantity)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 10:07
-	1046	(memory same control\$4 same register same (number quantity)) same pag\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 10:07
-	3	((memory same control\$4 same register same (number quantity)) same pag\$3) same ((flexible variable settable) with size)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 10:15
-	2	dynamic same buffer same supervising same system	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 11:30
-	8	d-channel same variable same frame	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 11:38
-	5264	(variable adj length) same frame	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 11:38
-	1	(communication communicat\$3) same data same frame same variable same bus same asynchronous\$2 same microprocessor same controller same (buffer fifo memory) same access same register\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 11:48

-	163	(lapd adj protocol) lapd-protocol	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 11:48
-	13	((lapd adj protocol) lapd-protocol) same buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 11:52
-	3	frame same asynchronous\$2 same (variable adj size) same (fifo buffer memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 12:06
-	0	(frame same asynchronous\$2 same (variable adj size) same (fifo buffer memory)) and 6208724.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 12:08
-	7	frame same asynchronous\$2 same (variable adj size)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 12:06
-	3884	(buffer memory fifo) same (mcu (memory adj ((control adj unit) controller))) same register\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 12:08
-	66	((buffer memory fifo) same (mcu (memory adj ((control adj unit) controller))) same register\$1) and atm	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 12:08
-	42	((((buffer memory fifo) same (mcu (memory adj ((control adj unit) controller))) same register\$1) and atm) and @ad < 19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 12:13
-	0	(quantlry adj2 data) same (size adj2 memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/02 12:13
-	236	memory same interrupt same (mcu (memory adj control adj unit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 11:14
-	5	(memory same interrupt same (mcu (memory adj control adj unit))) and (size same quantity)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 11:19
-	13	memory same (register with (quantity same size))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 11:27

-	0	memory same (to adj be adj read) same page\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 11:28
-	0	memory same (to adj be adj read)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 11:28
-	0	(memory same page\$3) and (to adj be adj read)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 11:28
-	0	(to adj be adj read)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 11:28
-	46943	memory same pag\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 11:29
-	348	paged adj memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 11:29
-	15	(paged adj memory) same cycle same (microprocessor processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 11:32
-	86	(paged adj memory) same (microprocessor processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 11:32
-	25	(paged adj memory) same (microprocessor processor) same register\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 11:33
-	1157	(mcu (memory adj controll)(memory adj control adj unit)) same register\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 11:35
-	216	((mcu (memory adj controll)(memory adj control adj unit)) same register\$1) and (pag\$3 with memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 11:35
-	168	((((mcu (memory adj controll)(memory adj control adj unit)) same register\$1) and (pag\$3 with memory)) and @ad<19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 12:04

-	20	(((((mcu (memory adj controll))(memory adj control adj unit)) same register\$1) and (pag\$3 with memory))) and @ad < 19980918) and (quantity same size)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 11:37
-	718	(variable adj size) same memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 11:51
-	41	(variable adj size) adj memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 11:52
-	69	lsdn same lapd same network same interface	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 12:04
-	52	(lsdn same lapd same network same interface) and @ad < 19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 12:08
-	950	(switching adj system) same lsdn	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 12:08
-	0	((switching adj system) same lsdn) same memory same register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 12:09
-	131	(data adj processor) same (address adj translation)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 13:02
-	29	(memory adj size) same (data with quantity)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 13:02
-	1232	setting with (memory same size)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 13:14
-	91	setting with (memory adj size)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 13:13
-	0	(setting with (memory adj size)) same quantity same read same (to adj be)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 13:14

-	0	(setting with (memory adj size)) same quantity same read	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 13:13
-	1	(setting with (memory adj size)) same quantity	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 13:13
-	95	setting with (cache same size)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 13:14
-	0	(setting with (cache same size)) same quantity same read same (to adj be)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 13:15
-	0	(setting with (cache same size)) same quantity same read	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 13:15
-	2	(setting with (cache same size)) same quantity	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 13:15
-	2353	memory and settable and size	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 14:29
-	7	memory and (settable adj size)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 14:32
-	0	(setting adj memory adj size) and (quantity adj2 data)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 14:32
-	7	setting adj memory adj size	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 15:02
-	94	fifo same buffer same full same interrupt same set\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 15:03
-	94	fifo same buffer same full same interrupt same set\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 15:06

-	77	(fifo same buffer same full same Interrupt same set\$5) and @ad < 19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:22
-	6	fifo same buffer same full same Interrupt same set\$5 same size	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 15:13
-	1	(set\$4 same (buffer adj (length size))) same (frame same (byte adj counter))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:43
-	16	(set\$4 same (buffer adj (length size))) and (frame same (byte adj counter))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 15:50
-	66796	determin\$3 same read same data	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:37
-	234	(determin\$3 same read same data) same (size adj3 memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:38
-	23540	determin\$3 same (read adj5 data)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:38
-	58	(determin\$3 same (read adj5 data)) same (size adj3 memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:38
-	43	((determin\$3 same (read adj5 data)) same (size adj3 memory)) and @ad < 19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:51
-	528	(memory adj size) same pag\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:41
-	1	((memory adj size) same pag\$3) and (network same frame same count\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:46
-	13	((memory adj size) same pag\$3) and (network same frame)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:48

-	0	framd same size same count\$3) same(memory same read	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:48
-	158	frame same size same count\$3) same(memory same read	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:50
-	166	((fifo buffer) adj2 size) same count\$3 same (frame packet\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:52
-	102	((fifo buffer) adj2 size) same count\$3 same (frame packet\$1)) and @ad < 19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:54
-	3465	(receiv\$3 same count\$3 same (packet\$1 frame\$1) same (fifo\$1 buffer\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:56
-	2707	((receiv\$3 same count\$3 same (packet\$1 frame\$1) same (fifo\$1 buffer\$3))) and communicat\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:57
-	2707	((receiv\$3 same count\$3 same (packet\$1 frame\$1) same (fifo\$1 buffer\$3))) and communicat\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:54
-	555	((receiv\$3 same count\$3 same (packet\$1 frame\$1) same (fifo\$1 buffer\$3))) and (network adj interface)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:58
-	427	((receiv\$3 same count\$3 same (packet\$1 frame\$1) same (fifo\$1 buffer\$3))) and (network adj interface)) and @ad < 19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:57
-	134	(receiv\$3 same count\$3 same (packet\$1 frame\$1) same (fifo\$1 buffer\$3) same updat\$3 same (controller processor microprocessor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:57
-	96	((receiv\$3 same count\$3 same (packet\$1 frame\$1) same (fifo\$1 buffer\$3) same updat\$3 same (controller processor microprocessor))) and @ad < 19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:23
-	109	((receiv\$3 same count\$3 same (packet\$1 frame\$1) same (fifo\$1 buffer\$3) same updat\$3 same (controller processor microprocessor))) and communicat\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:58

-	109	((receiv\$3 same count\$3 same (packet\$1 frame\$1) same (fifo\$1 buffer\$3) same updat\$3 same (controller processor microprocessor))) and communicat\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:58
-	39	((receiv\$3 same count\$3 same (packet\$1 frame\$1) same (fifo\$1 buffer\$3) same updat\$3 same (controller processor microprocessor))) and communicat\$5) and (network adj interface)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 16:58
-	33	((receiv\$3 same count\$3 same (packet\$1 frame\$1) same (fifo\$1 buffer\$3) same updat\$3 same (controller processor microprocessor))) and communicat\$5) and (network adj interface)) and @ad<19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:29
-	0	count\$3 adj to adj be adj read	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:09
-	231679	(quantity adj\$5 data) same read same (memory buffer\$1 fifo\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:10
-	107620	((quantity adj\$5 data) same read same (memory buffer\$1 fifo\$1)) and ((network communication) adj\$3 interface)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:11
-	29028	(mcu (memory adj control adj unit) (memory controller)) same (count\$3 quantity) same data same read same (memory buffer\$1 fifo\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:13
-	18572	(mcu (memory adj control adj unit) (memory controller)) with (count\$3 quantity) same data same read same (memory buffer\$1 fifo\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:14
-	9384	(mcu (memory adj control adj unit) (memory controller)) with (count\$3 quantity) with data with read with (memory buffer\$1 fifo\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:14
-	23703	network adj interface	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:19
-	118	(network adj interface) and (receiv\$3 with (byte\$1 adj1 count\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:20
-	7	(network adj interface) same (receiv\$3 with (byte\$1 adj1 count\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:21

-	98	((network adj Interface) and (receiv\$3 with (byte\$1 adj1 count\$3))) and (data same read same (fifo\$1 memory buffer\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:22
-	79	((network adj Interface) and (receiv\$3 with (byte\$1 adj1 count\$3))) and (data same read same (fifo\$1 memory buffer\$1))) and @ad < 19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:25
-	72	paged adj memory adj management	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:25
-	0	(paged adj memory adj management) and (receive same read same (fifo buffer) same variable same count same size)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:27
-	2467	buffer same size same (flexible variable settable)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:31
-	20675	settable	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:28
-	1434	(buffer same size same (flexible variable settable)) and (network interface)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:32
-	0	(buffer same size same (flexible variable settable)) and (ISND same LPDA same (network interface))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:29
-	0	(buffer same size same (flexible variable settable)) and (ISND same (network interface))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:29
-	1434	(buffer same size same (flexible variable settable)) and (network interface)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:29
-	328	(buffer same size same (flexible variable settable)) same (network interface)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:29
-	201	((buffer same size same (flexible variable settable)) same (network interface)) and @ad < 19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:38

-	291	buffer same size same (flexible variable settable) same count\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:32
-	201	(buffer same size same (flexible variable settable) same count\$3) and (network interface)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:32
-	179	(buffer same size same (flexible variable settable) same count\$3) and @ad < 19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:36
-	89	buffer same size same set\$5 same ahead	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:41
-	45	(buffer same size same set\$5 same ahead) and @ad < 19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 19:05
-	41	buffer same size same count\$3 same set\$5 same ahead	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:42
-	19	buffer same size same read same count\$3 same set\$5 same ahead	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 17:44
-	33	quantity same read same received same buffer same count\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 19:03
-	29	(quantity same read same received same buffer same count\$3) and @ad < 19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 19:10
-	280	optimiz\$5 same (fifo\$1 buffer\$1) adj3 (allocat\$3 size quantity)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 19:10
-	75	(optimiz\$5 same (fifo\$1 buffer\$1) adj3 (allocat\$3 size quantity)) same set\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 19:10
-	47	((optimiz\$5 same (fifo\$1 buffer\$1) adj3 (allocat\$3 size quantity)) same set\$5) and @ad < 19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 19:17

-	39	((optimiz\$5 same (fifo\$1 buffer\$1) adj3 (allocat\$3 size quantity)) same set\$5) and setting	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 19:13
-	0	((optimiz\$5 same (fifo\$1 buffer\$1) adj3 (allocat\$3 size quantity)) same set\$5) and setting) and (quantity same data same read\$3 same receiv\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 19:14
-	4	((optimiz\$5 same (fifo\$1 buffer\$1) adj3 (allocat\$3 size quantity)) same set\$5) and setting) and quantity and (data same read\$3 same receiv\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 19:15
-	81	quantity same (buffer adj size)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 19:16
-	11	quantity same (buffer adj size) same set\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 19:17
-	7	(quantity same (buffer adj size) same set\$5) and @ad<19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 19:43
-	3343	reception adj (fifo\$1 memory buffer\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/04 02:13
-	0	(reception adj (fifo\$1 memory buffer\$1)) same (settable adj memory)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 20:22
-	322	(reception adj (fifo\$1 memory buffer\$1)) same (count size)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 20:23
-	8	(reception adj (fifo\$1 memory buffer\$1)) same (count same size)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 23:38
-	0	(Interrupt same (mcu (memory adj controller) (memory adj control adj unit)) same accept\$3 same (new adj data) same (ask\$3 request\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 23:40
-	0	(Interrupt same (mcu (memory adj controller) (memory adj control adj unit)) same accept\$3 same (new adj data) same (ask\$3 request\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 23:40

-	11	(interrupt same (mcu (memory adj controller) (memory adj control adj unit)) same accept\$3 same (ask\$3 request\$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 23:40
-	674	buffer same receiv\$3 same interrupt same request\$3 same read\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 23:47
-	7	buffer same receiv\$3 same interrupt same request\$3 same read\$3 same (mcu (memory adj controller) (memory adj control adj unit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 23:50
-	1	interrupt same (mcu (memory adj controller) (memory adj control adj unit)) same (microprocessor processor) same (memory adj3 ready)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 23:53
-	5	interrupt same (mcu (memory adj controller) (memory adj control adj unit)) same (memory adj3 ready)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 23:53
-	1345	interrupt same (mcu (memory adj controller) (memory adj control adj unit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 23:57
-	262	interrupt same (mcu (memory adj controller) (memory adj control adj unit)) and ((request\$3 ask\$3) same ready)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 23:55
-	205	(interrupt same (mcu (memory adj controller) (memory adj control adj unit)) and ((request\$3 ask\$3) same ready)) and @ad<19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 23:55
-	160	interrupt same (mcu (memory adj controller) (memory adj control adj unit)) and ((request\$3 ask\$3) same ready same (buffer fifo\$1 memory))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 23:58
-	122	(interrupt same (mcu (memory adj controller) (memory adj control adj unit)) and ((request\$3 ask\$3) same ready same (buffer fifo\$1 memory))) and @ad<19980918	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 23:56
-	404	interrupt same (mcu (memory adj controller) (memory adj control adj unit)) same Interface	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 23:57
-	7	interrupt same (mcu (memory adj controller) (memory adj control adj unit)) same (network adj interface)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/03 23:57

-	2	Interrupt same (mcu (memory adj controller) (memory adj control adj unit)) and ((request\$3 ask\$3) same ready same (buffer fifo\$1 memory)) same (network adj Interface)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/04 00:03
-	129	(buffer fifo\$1) adj3 ready adj3 accept\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/04 00:04
-	4	((buffer fifo\$1) adj3 ready adj3 accept\$3) same Interrupt	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/11/04 00:15